

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Rajesh Khamankar, et al.	Docket No:	TI-33223
Serial No:	Not Assigned	Conf. No:	Not Assigned
Examiner:	TBD	Art Unit:	TBD
Filed:	11/06/2003		
For:	RELIABLE HIGH VOLTAGE GATE DIELECTRIC LAYERS USING A DUAL NITRIDATION PROCESS		

Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

"EXPRESS MAIL" Mailing Label No. EV 333322681 US  
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage for "Express Mail Post Office to Addressee" service under 37 CFR 1.10 and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on 11-6-2003.

  
Ann Trent

**PRELIMINARY AMENDMENT**

Dear Sir:

Prior to the examination of the above identified application, please amend the specification by inserting before the first line the sentence:

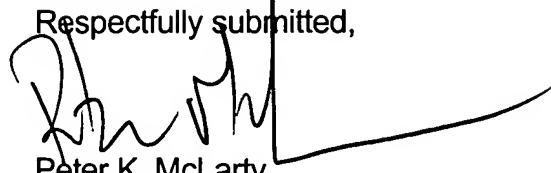
--This application claims priority under 35 USC § 119(e)(1) of provisional application Serial No. 60/428,871, filed November 25, 2002.--

Should the Examiner have any comments or suggestions concerning this application, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicants petition for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including

extension of time fees, to the deposit account of Texas Instruments Incorporated,  
Account No. 20-0668.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Peter K. McLarty', with a long horizontal flourish extending to the right.

Peter K. McLarty  
Attorney for Applicants  
Reg. No. 44,923

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